

INTEGRATED CIRCUIT MEMORY SYSTEMS HAVING PROGRAMMABLE  
SIGNAL BUFFERS FOR ADJUSTING SIGNAL TRANSMISSION DELAYS AND  
METHODS OF OPERATING SAME

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ABSTRACT OF THE DISCLOSURE

An integrated circuit memory system includes one or more memory modules  
in which at least one of the memory modules is responsive to a control signal and has  
delay control information stored thereon. The memory system further includes a  
memory controller that is configured to generate the control signal in response to the  
10 delay control information.